

ABSTRACT OF THE DISCLOSURE

A bus system is disclosed for use with switching devices, such as power electronic devices. The system includes generally parallel bus elements that define electrical reference planes, such as for a dc bus. The bus elements are separated from one another by insulative layers, with additional insulative layers being available for separating the system from other circuit components. Portions of the bus elements are extended or exposed to permit connection to the circuit elements, including packaged switching circuits and energy storage or filtering circuits. The bus system may be conformed to a variety of geometric configurations, and substantially reduces parasitic inductance and total loop inductance in the resulting circuitry.